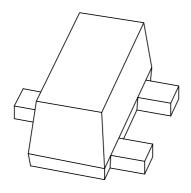
DISCRETE SEMICONDUCTORS

DATA SHEET



1PS89SB74 Schottky barrier double diode

Product specification

2001 Apr 20





Schottky barrier double diode

1PS89SB74

FEATURES

- Low forward voltage
- High breakdown voltage
- · Guard ring protected
- Ultra small plastic SMD package
- Low capacitance.

APPLICATIONS

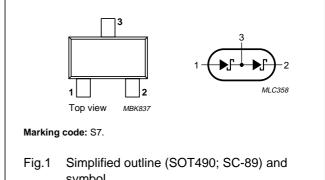
- Ultra high-speed switching
- Voltage clamping
- · Protection circuits
- · Blocking diodes.

DESCRIPTION

Planar Schottky barrier diode encapsulated in a SOT490 (SC-89) ultra small plastic SMD package.

PINNING

PIN	DESCRIPTION
1	anode (a ₁)
2	cathode (k ₂)
3	common (k ₁ , a ₂)



symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
Per diode ι	Per diode unless otherwise specified					
V _R	continuous reverse voltage		_	70	V	
I _F	continuous forward current		-	70	mA	
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s}; \ \delta \le 0.5$	-	70	mA	
I _{FSM}	non-repetitive peak forward current	t _p < 10 ms	_	100	mA	
P _{tot}	total power dissipation (per package)	T _{amb} ≤ 25 °C	_	200	mW	
T _{stg}	storage temperature		-65	+150	°C	
Tj	junction temperature		_	+150	°C	
T _{amb}	operating ambient temperature		-65	+150	°C	

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Schottky barrier double diode

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ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT			
Per diode	Per diode						
V _F	continuous forward voltage	see Fig.2;					
		I _F = 1 mA	410	mV			
		I _F = 10 mA	750	mV			
		I _F = 15 mA	1	V			
I _R	continuous reverse current	V _R = 50 V; see Fig.3; note 1	100	nA			
		V _R = 70 V; see Fig.3; note 1	10	μΑ			
C _d	diode capacitance	V _R = 0; f = 1 MHz; see Fig.4	2	pF			

Note

1. Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

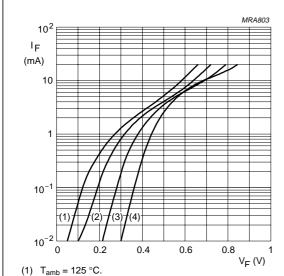
Note

1. Refer to SOT490 (SC-89) standard mounting conditions.

Schottky barrier double diode

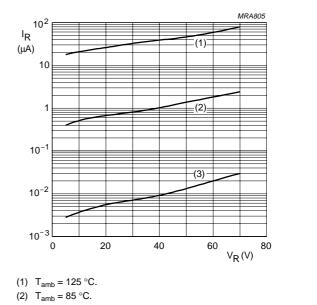
1PS89SB74

GRAPHICAL DATA



- (2) $T_{amb} = 85 \,^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.
- (4) $T_{amb} = -40 \, ^{\circ}C$.

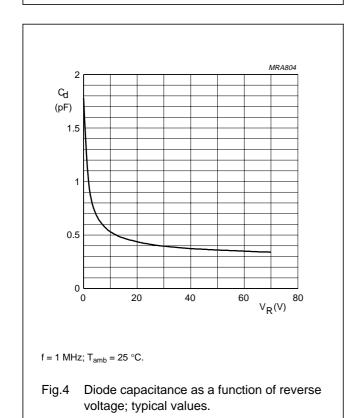
Fig.2 Forward current as a function of forward voltage; typical values.



(3) $T_{amb} = 25 \, ^{\circ}C$.

4

Fig.3 Reverse current as a function of reverse voltage; typical values.



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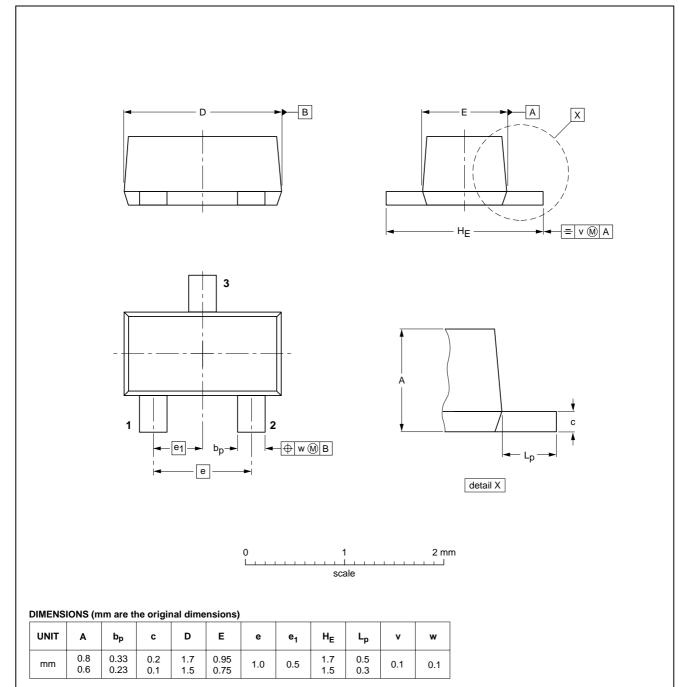
Schottky barrier double diode

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT490



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT490			SC-89			98-10-23

Schottky barrier double diode

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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

Notes

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